

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

APPARATUS AND METHOD FOR ELECTRONIC FUSE WITH
IMPROVED ESD TOLERANCE

Application Number :

Confirmation Number:

First Named Applicant: Steven Voldman

Attorney Docket Number: BUR920030119US1

Art Unit:

Examiner:

Search string: (4515981 or 5936880 or 6580156 or 6525397 or 6088256 or 6368902 or 6507087
or 4748491 or 20030025177 or 20020102839 or 20020086462).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

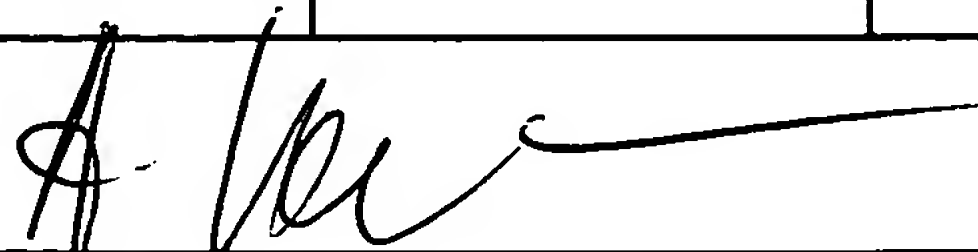
| init/ | Cite.No. | Patent No. | Date | Patentee | Kind | Class | Subclass |
|-------------------------------------|----------|------------|------------|------------------|------|-------|----------|
| <input checked="" type="checkbox"/> | 1 | 4515981 | 1985-05-21 | Schlupp | | | |
| <input checked="" type="checkbox"/> | 2 | 5936880 | 1999-08-10 | Payne | | | |
| <input checked="" type="checkbox"/> | 3 | 6580156 | 2003-06-17 | Ito et al. | | | |
| <input checked="" type="checkbox"/> | 4 | 6525397 | 2003-02-25 | Kalnitsky et al. | | | |
| <input checked="" type="checkbox"/> | 5 | 6088256 | 2000-07-11 | Worley et al. | | | |
| <input checked="" type="checkbox"/> | 6 | 6368902 | 2002-04-09 | Kothandaraman | | | |
| <input checked="" type="checkbox"/> | 7 | 6507087 | 2003-01-14 | Yu | | | |
| <input checked="" type="checkbox"/> | 8 | 4748491 | 1988-05-31 | Takagi | | | |

US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

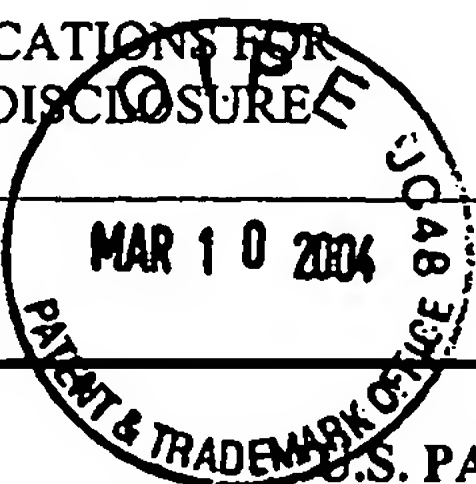
| init/ | Cite.No. | Pub. No. | Date | Applicant | Kind | Class | Subclass |
|-------------------------------------|----------|-------------|------------|-------------------------|------|-------|----------|
| <input checked="" type="checkbox"/> | 1 | 20030025177 | 2003-02-06 | Kothandaraman | | | |
| <input checked="" type="checkbox"/> | 2 | 20020102839 | 2002-08-01 | Gonzalez et al. | | | |
| <input checked="" type="checkbox"/> | 3 | 20020086462 | 2002-07-04 | Kothandaraman et al. | | | |

Signature



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| SUPPLEMENTAL FORM PTO-1449 (Modified) | | | ATTY. DOCKET NO. BUR920030119US1 | | SERIAL NO. Unassigned <i>10/707,283</i> | |
| LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT | | | APPLICANT: Steven H. Voldman | | | |
| (Use several sheets if necessary) | | | FILING DATE: <i>12/03/03</i> Concurrently Herewith | | GROUP: Unassigned | |



| REFERENCE DESIGNATION | | | | | | | |
|-----------------------|--------------------|------|------|-------|----------|----------------------------|--|
| EXAMINER INITIALS | DOCUMENT NUMBER | DATE | NAME | CLASS | SUBCLASS | FILING DATE (IF APPRO.) | |
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| FOREIGN PATENT DOCUMENTS | | | | | | | |
| DOCUMENT NUMBER | DATE | COUNTRY | CLASS | SUBCLASS | TRANSLATION | | |
| | | | | | YES | NO | |
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| | | | | | | | |
| OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.) | | | | | | | |
| *A-V* | | | W. J. Craig, et al., "On-Chip Electrically Programmable Fuse", IBM Technical Disclosure Bulletin, Vol. 29, No. 3, August 1986. | | | | |
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| EXAMINER <i>A-V</i> | DATE CONSIDERED <i>01/5/06</i> |
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

STATEMENT
NOV 28 2005
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